

Appendix I

INCLUDE "16F819.INC"

; Define statements.

#define CHAR_PACING 1000
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#define INTHAND ISERV

RAM_START EQU 00020h
RAM_END EQU 0016Fh
RAM_BANKS EQU 00003h
BANK0_START EQU 00020h
BANK0_END EQU 0007Fh
BANK1_START EQU 000A0h
BANK1_END EQU 000EFh
BANK2_START EQU 00120h
BANK2_END EQU 0016Fh
EEPROM_START EQU 02100h
EEPROM_END EQU 021FFh

wsave EQU RAM_START + 000h
_CCPX EQU RAM_START + 001h
_OTIME EQU RAM_START + 003h
R0 EQU RAM_START + 005h
R1 EQU RAM_START + 007h
R2 EQU RAM_START + 009h
R3 EQU RAM_START + 00Bh
R4 EQU RAM_START + 00Dh
R5 EQU RAM_START + 00Fh
R6 EQU RAM_START + 011h
R7 EQU RAM_START + 013h
R8 EQU RAM_START + 015h
T1 EQU RAM_START + 017h
_XTEMP EQU RAM_START + 019h
_ACNT EQU RAM_START + 01Bh
FLAGS EQU RAM_START + 01Ch
GOP EQU RAM_START + 01Dh
psave EQU RAM_START + 01Eh
RM1 EQU RAM_START + 01Fh
RM2 EQU RAM_START + 020h
RR1 EQU RAM_START + 021h
RR2 EQU RAM_START + 022h
ssave EQU RAM_START + 023h
_AMP EQU RAM_START + 024h
_CURRENT EQU RAM_START + 026h
_L20 EQU RAM_START + 028h
_LCURRENT EQU RAM_START + 02Ah
_POTAVG EQU RAM_START + 02Ch
_PVOLT EQU RAM_START + 02Eh
_VIDLE EQU RAM_START + 030h
_VMAX EQU RAM_START + 032h
_VOLTS EQU RAM_START + 034h
_VOUT EQU RAM_START + 036h
_X2 EQU RAM_START + 038h
_Y2 EQU RAM_START + 03Ah
_Z2 EQU RAM_START + 03Ch
_ALoop EQU RAM_START + 03Eh
_BCNT EQU RAM_START + 03Fh
_CCNT EQU RAM_START + 040h
_ELoop EQU RAM_START + 041h
_LCNT EQU RAM_START + 042h
_MISCF EQU RAM_START + 043h
_N EQU RAM_START + 044h
_PCNT EQU RAM_START + 045h
_PCNT1 EQU RAM_START + 046h
_X EQU RAM_START + 047h
_Y EQU RAM_START + 048h